

--Abstract

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5 An unpredictable microprocessor or microcomputer  
comprises a processor (1), a first working memory (51), a main  
memory (6) containing an operating system, a main program (P1)  
and a secondary program (P2), a second working memory (52), and  
switching means which, during the performance of the programs,  
makes it possible to switch from using one of the two working  
memories (51, 52) to using the other working memory, while  
preserving their contents. Switching means comprise at least  
10 one first block of registers (54) for storing the operating  
context of the programs in the main memory and a switching  
circuit (53) for enabling one of the working memories and the  
access registers (A1-a3) (d1-d3) associated with each memory  
(51, 52, 6) and controlled by said switching circuit (53).--